REMARKS

Claims 1-25 are pending in the application with claims 9-19 and 23-25 withdrawn from consideration. Claims 1-8 and 20-22 are rejected. Claim 1 has been amended. Claims 3-8 and 20-22 have been cancelled.

The drawings were objected to under 37 CFR 1.83(a). The claims related to this objection have been cancelled and the drawings are now believed to be compliance with 37 CFR 1.83(a).

Claims 3-5, 7-8, and 22 were rejected under 35 USC 112, second paragraph. These claims have been cancelled.

Claim 1 was rejected under 35 USC 102(b) as being anticipated by Branson. Claim 1, as amended, includes "...wherein the regenerative latch comprises a first transistor coupled between the pair of transistors, a second transistor coupled to a first one of the pair of transistors, and a third transistor coupled to a second one of the pair of transistors, wherein the first, second, and third transistors are controlled by a clock signal node." The references of record do not show, teach, or suggest the above recited limitations of claim 1. Claim 2 depends from claim 1. Therefore, claims 1 and 2 are believed to be allowable over the

TI-37350 Page 7

references of record.

It is believed that the above remarks and amendments are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

Álan Stewart

Registration No. 35,373

Texas Instruments, Incorporated P. O. Box 655474 - M/S 3999 Patent Department Dallas, Texas 75265